

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Priority Application Serial No. 09/954,340
Priority Filing Date September 14, 2001
 Inventor Tyler A. Lowrey et al.
 Assignee Micron Technology, Inc.
Priority Group Art Unit 2812
Priority Examiner J.M. Kennedy
 Attorney's Docket No. MI22-2348
 TITLE: Methods of Forming Capacitors, Methods of Forming Capacitor-Over-Bit Line
 Memory Circuitry, and Related Integrated Circuitry Constructions

Commissioner for Patents
 Alexandria, VA 22313-1450

Attention: Official Draftsman

SUBSTITUTE DRAWING REQUEST

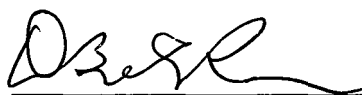
Please enter the enclosed substitute drawings in the above-referenced application in place of drawings originally filed. The content of the drawings are identical to those now on file in this application.

Acknowledgment of receipt of the formal drawings and their acceptance into the file is requested.

Respectfully submitted,

Date: 7-25-03

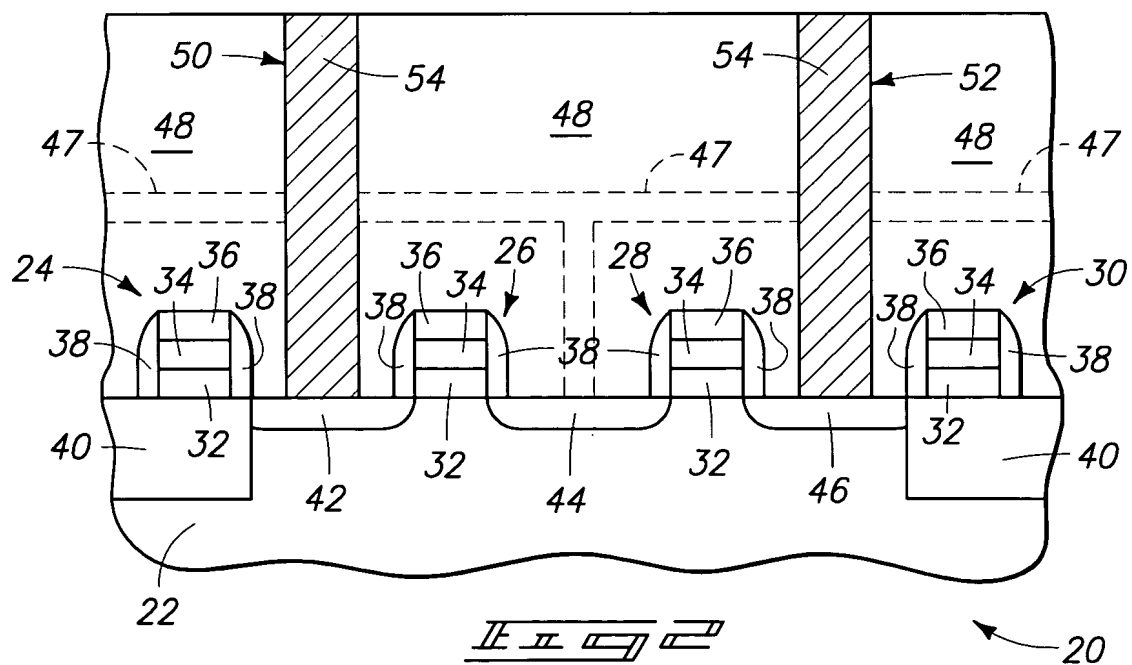
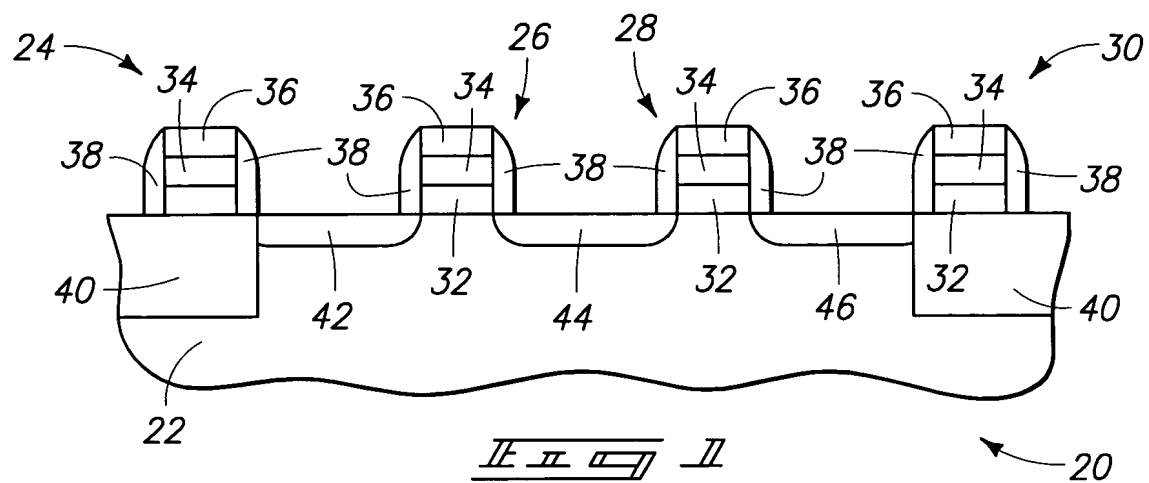
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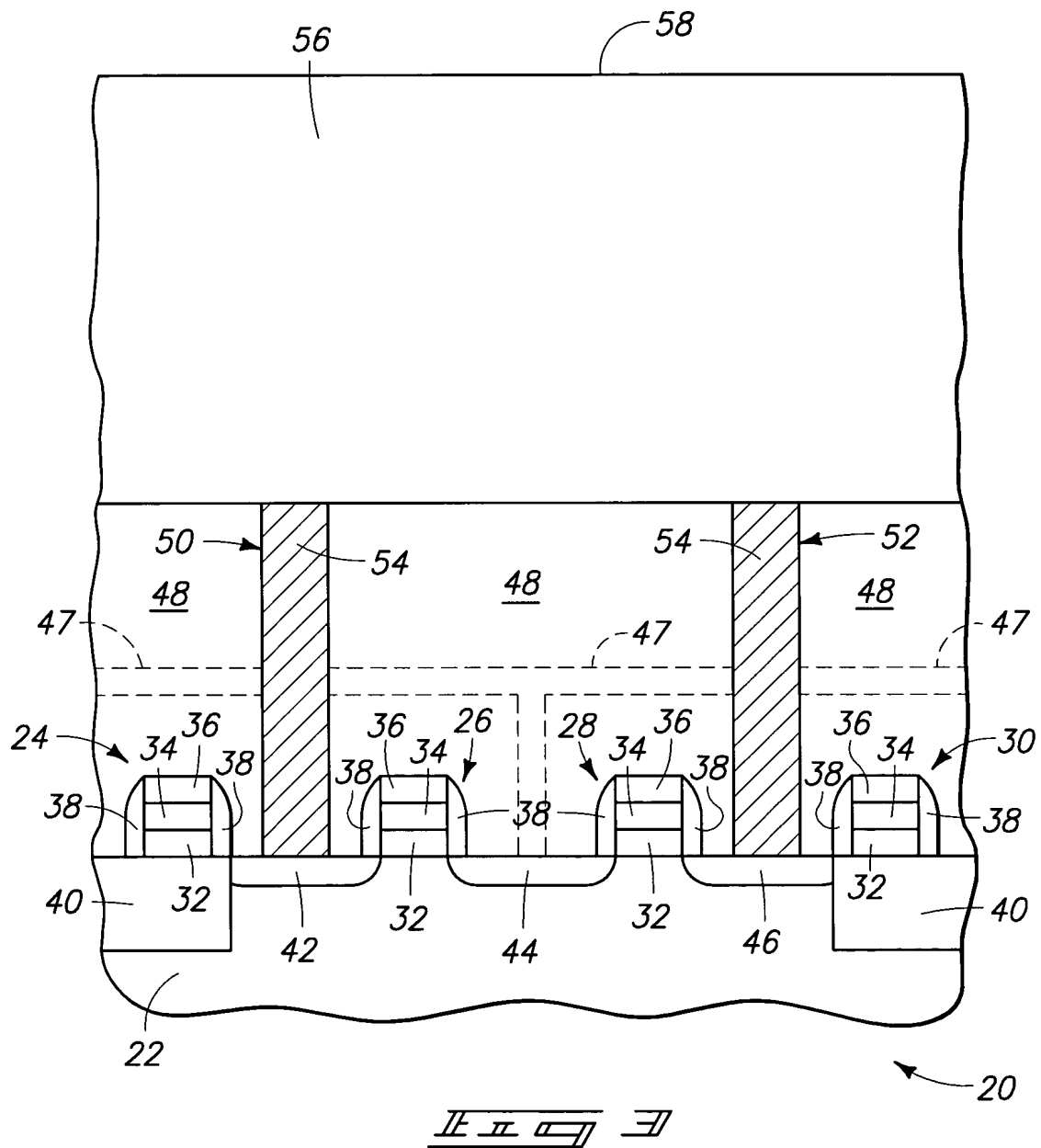
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 601 W. First Avenue, Suite 1300
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 (509) 624-4276

Enclosures: 11 Sheets of Formal Drawings, Figs. 1-13.

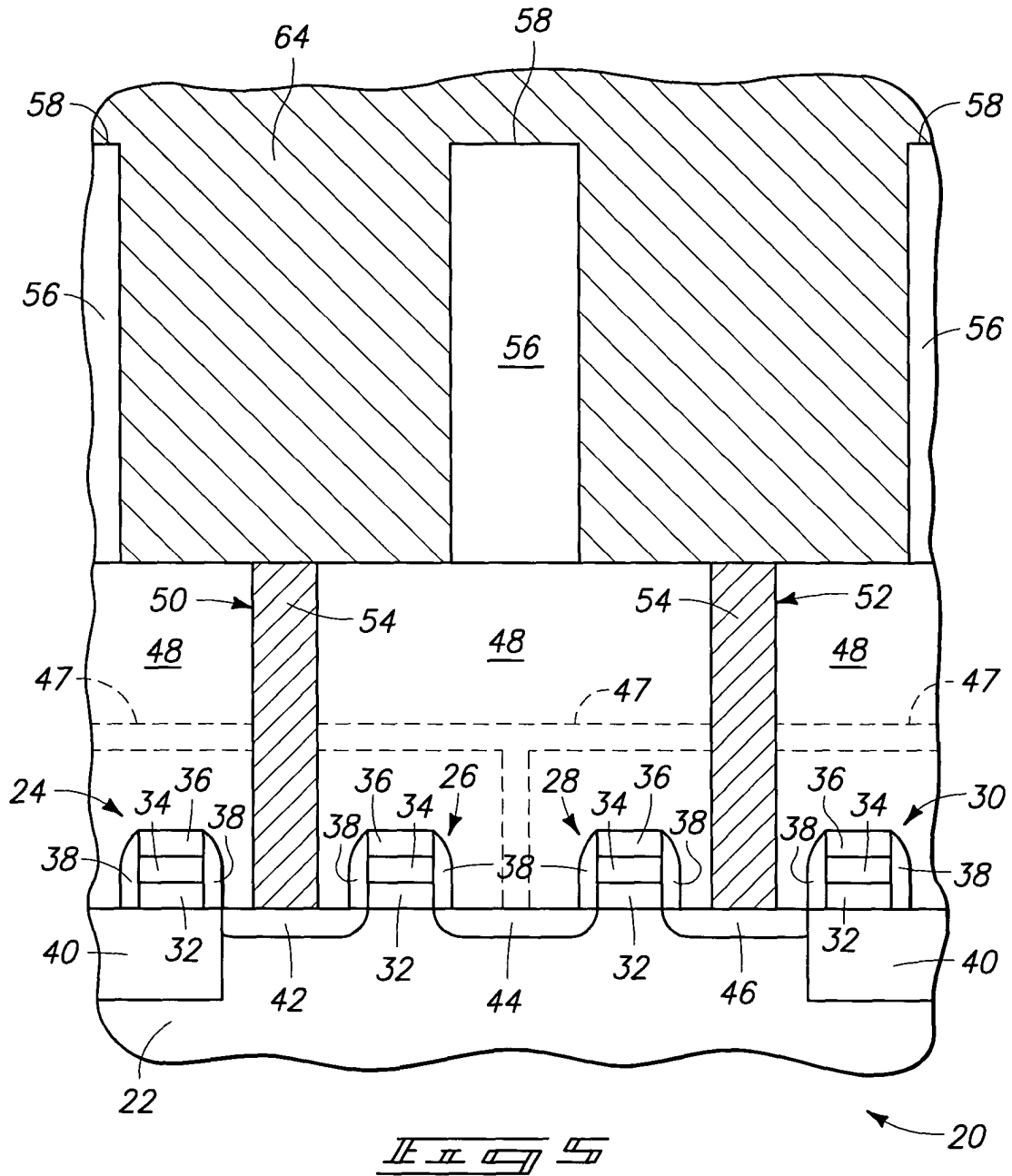
1/11



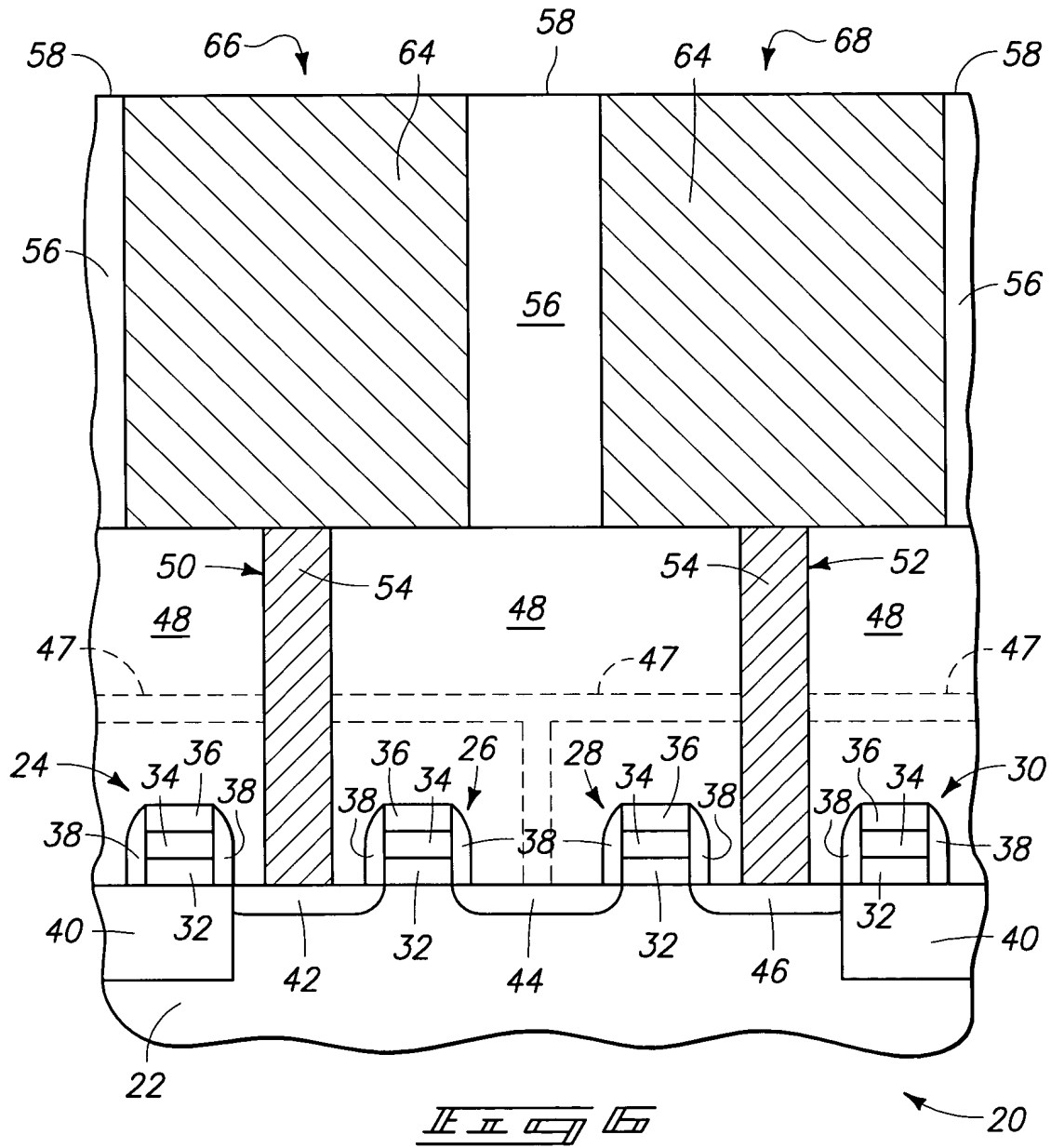
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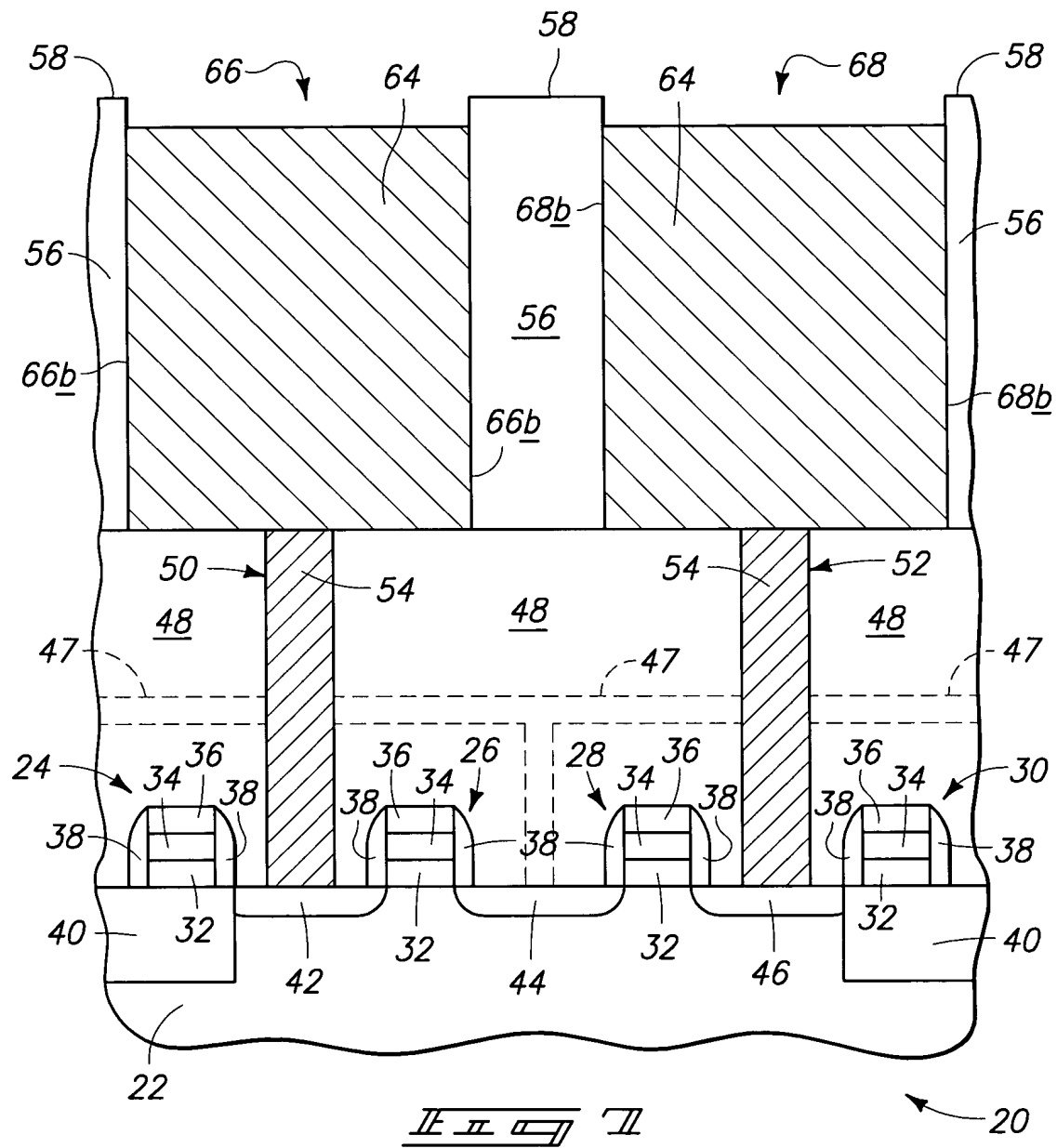
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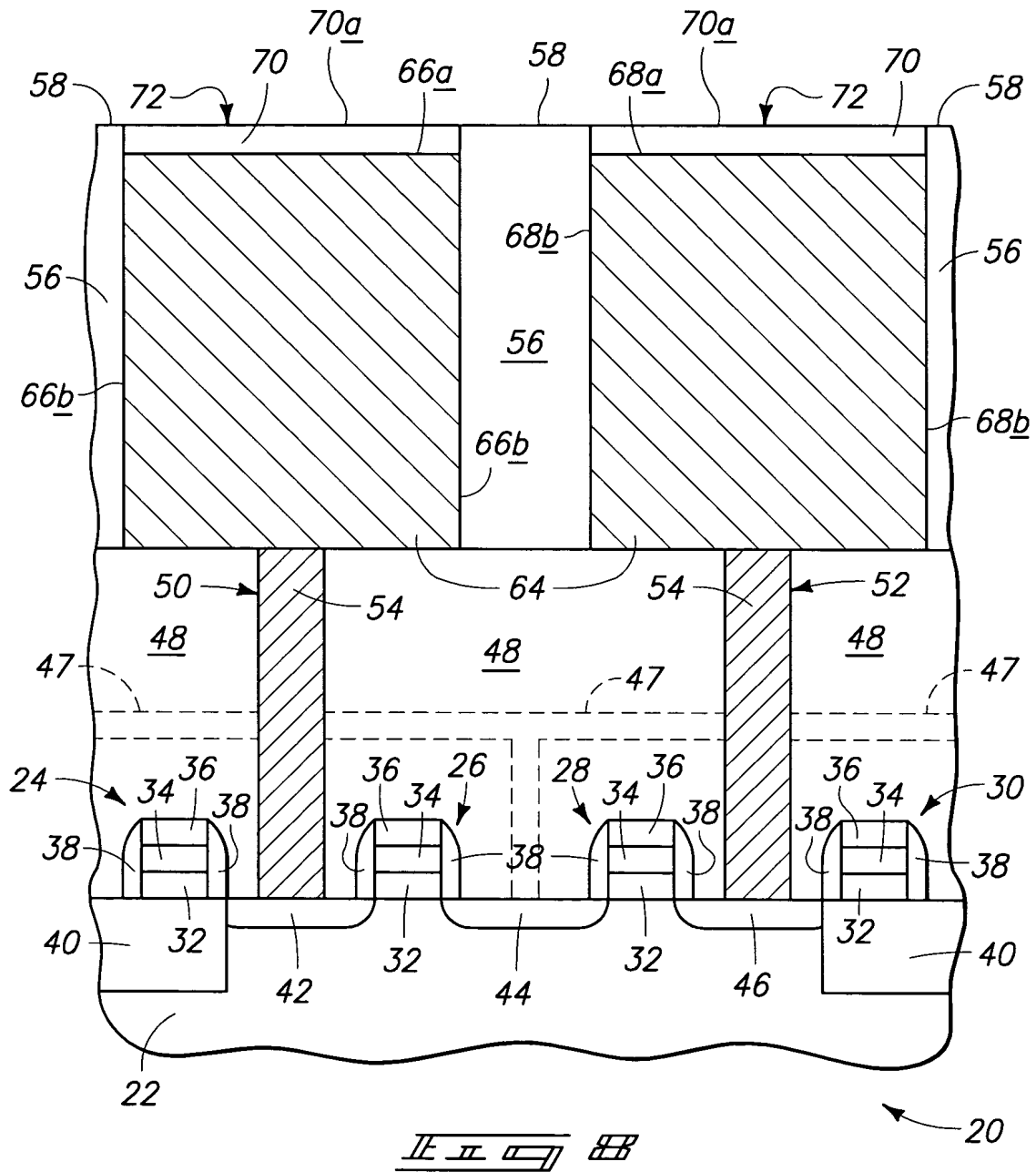
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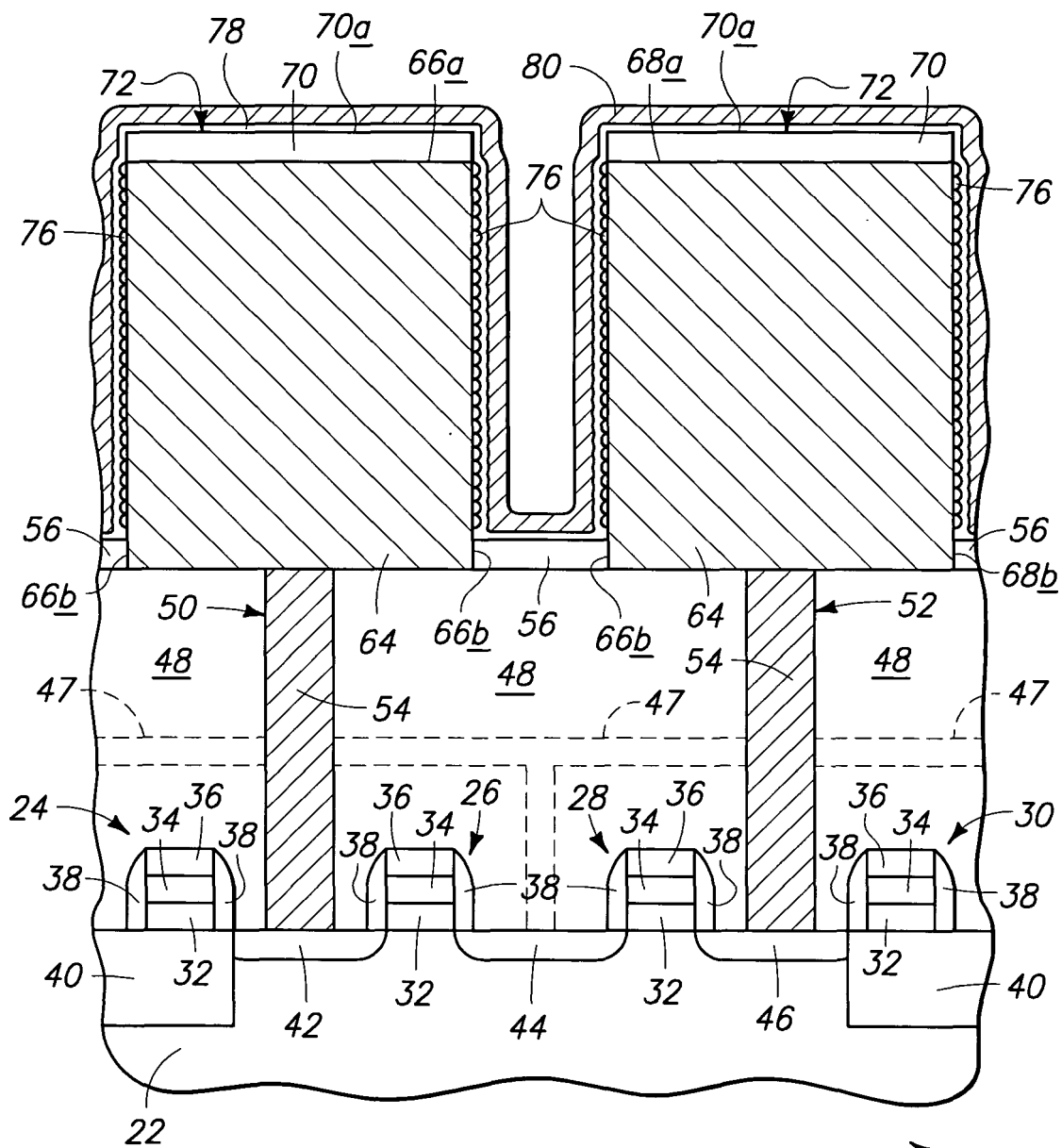
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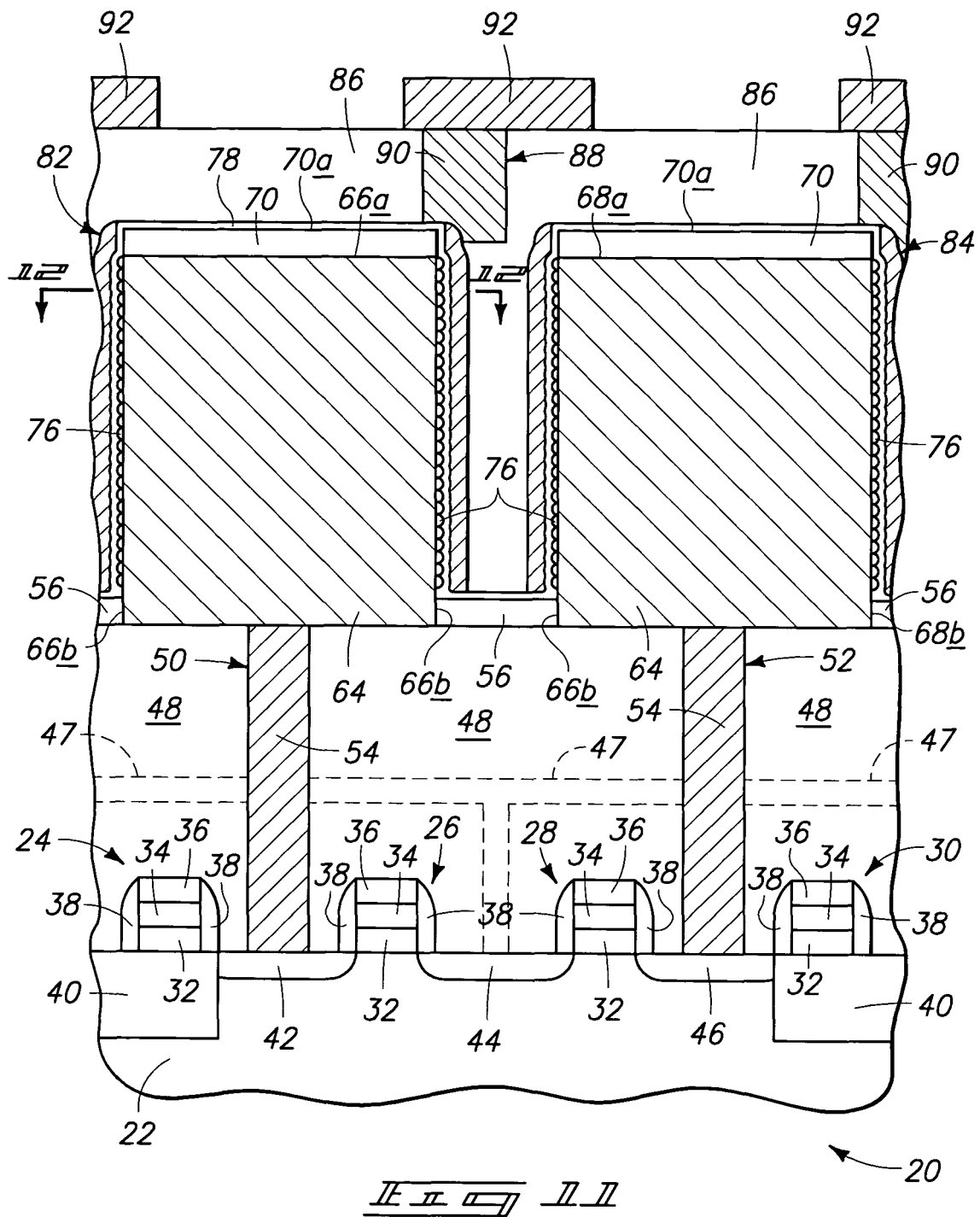
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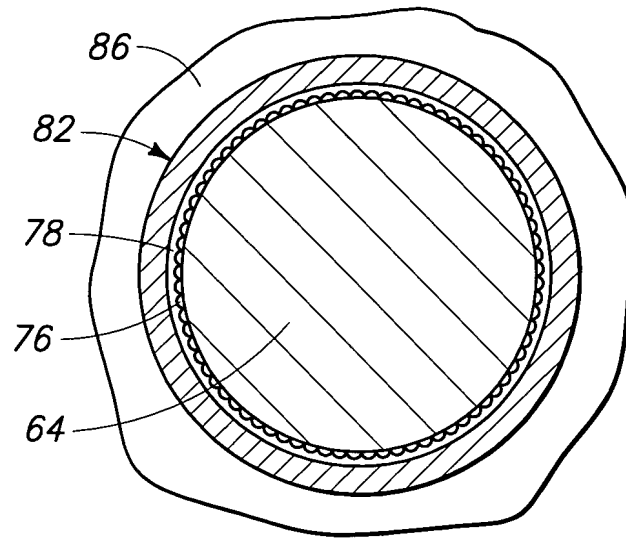


FIG. 11

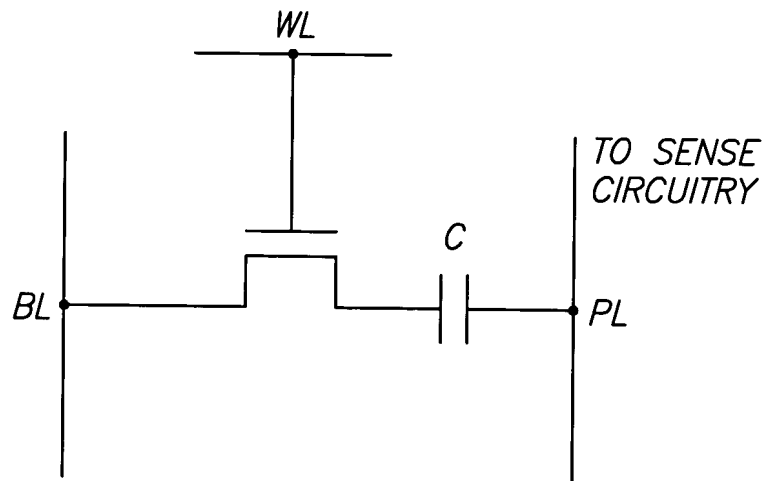


FIG. 12